

IN THE SPECIFICATION:

Amend the specification as follows:

Heading beginning at page 1, line 13 has been amended as follows:

Description of the Background Related Art

Paragraph beginning at page 28, line 7 has been amended as follows:

Fig. 5 is a circuit diagram showing an example of a configuration of a channel width varying circuit which can realize the operational amplifier design resource reuse method of Fig. 1.

Paragraph beginning at page 28, line 22 has been amended as follows:

As shown above, an arbitrary number of ~~transistor~~ or transistors, 5a, 5b, 5c, can be connected in parallel between the common node 10 and the ground terminal by turning on/off the switches 51, 52 and 53 with the switch signals S1, S2 and S3. The channel width of the current-source transistor 5 can thus be varied among a plurality of kinds of values.

Paragraph beginning at page 30, line 25 has been amended as follows:

As shown above, an arbitrary number of ~~transistor~~ or transistors, 16a, 16b, 16c, can be connected in parallel between the output node 14 and the ground terminal by turning on/off the switches 161, 162, and 163 with the switch signals S11, S12 and S13. The bias voltage NBias

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can thus be adjusted.